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**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:** Luo et al.

**Serial No.:** 10/826,985

**Filed:** April 19, 2004

**For:** METHODS FOR FORMING  
PROTECTIVE LAYERS ON  
SEMICONDUCTOR DEVICE COMPONENTS  
SO AS TO REDUCE OR ELIMINATE THE  
OCCURENCE OF DELAMINATION THEREOF  
AND CRACKING THEREIN

**Confirmation No.:** 3493

**Examiner:** J. Stark

**Group Art Unit:** 2823

**Attorney Docket No.:** 2269-5565.1US  
(02-1124.01/US)

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

February 8, 2007  
Date

Signature

Erika Gandre  
Name (Type/Print)

**RESPONSE TO FINAL OFFICE ACTION**

Mail Stop AF  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

**Examiner:**

This Response follows the final Office Action of January 23, 2007, the shortened statutory period for response to which expires on April 23, 2007. This Response is being submitted within two months of the mailing date of the final Office Action.